Form PTO-1449 (Modified) PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT Use several sheets if necessary



ATTY. DOC 83175 APPLICANT

May 9, 2001

Tak Kin Chu et al. FILING DATE

2813

GROUP ART UNIT

SERIAL NO.

09/853,925

	REFE	RENCE DESIGN	IATION	U.S. PATENT DOC	UMENTS		
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DAT
EK	AA	5,690,737	11/25/97	Santiago et al.	117	92	THE ROLL AND A
<u> </u>	AB	5,695,810	12/9/97	Dubin et al.	427	96	
	AC	5,824,599	10/20/98	Schacham-Diamand et al.	438	678	
	AD	5,225,031	7/6/93	McKee et al.	156	612	
	AE	5,248,633	9/28/93	Morar et al.	437	196	
	AF	5,378,905	1/3/95	Nakamura	257	213	'
	AC	5,482,003	1/9/96	McKee et al.	117	108	
	AH	5,569,538	10/29/96	Cho	428	427	EC
	AI	5,593,951	1/14/97	Himpsel	505	235	TECHNOLOGY:
	AJ	5,637,533	6/10/97	Choi	438	643	JUL I
	AK	5,670,420	9/23/97	Choi	437	189	R 8 X
	AL	5,677,572	10/14/97	Hung et al.	257	750	
	AM	5,696,018	12/9/97	Summerfelt et al.	437	60	2800
	AN	5,753,040	5/19/98	Cho	117	106	1.0
	AO	5,773,085	6/30/98	Inoue et al.	427	255.2	
	AP	5,824,590	10/20/98	New	438	393	
	AQ	4,291,327	9/22/81	Tsang	357	52	
	AR	4,550,331	10/29/85	Milano	357	24	
	AS	4,692,993	9/15/87	Clark et al.	437	53	
	AT.	4,847,666	7/11/89	Heremans et al.	357	16	
	AU	4,915,746	4/10/90	Welsch	148	4	
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	AW	5,151,168	9/29/92	Gilton et al.	205	123	
	AX	5,435,264	7/25/95	Santiago et al.	117	92	
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BB ;	851,483	7/1/98	EPO			Yes			
BC ♥	881,673	12/2/98	EPO			Yes			
BD 1	2-266569	10/31/90	Japan		<del></del>	Abstract only			
BE ,	6-310509	11/4/94	Japan			Yes			
	BC ♥	NUMBER  BB ; 851,483  BC © 881,673  BD ; 2-266569	DOCUMENT DATE  BB i 851,483 7/1/98  BC © 881,673 12/2/98  BD i 2-266569 10/31/90	DOCUMENT   DATE   COUNTRY	DOCUMENT   DATE   COUNTRY   CLASS	DOCUMENT   DATE   COUNTRY   CLASS   SUBCLASS			

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P	OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)											
	Z/C 2003	Chaudhari et al., "Calcium Fluoride thin films on GaAs(100) for possible metal-insulator-semiconductor applications", Appl. Phys. lett., Vol. 62, No. 8, 22 Feb. 1993, pp. 852-854.										
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		BM D. Gardner et al., "Encapsulated Copper Interconnection Devices using Sidewall Barriers", Proc. 8 <sup>th</sup> Int' IEEE VLSI MVIC, June 1991, pp. 99-108.							rriers", Proc. 8 <sup>th</sup> Int'l			
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EXAMINER: Initial if reference considered whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Information Disclosure Statement - Section 9 PTO-1449 (Modified) [6-1] page 01 of 02)

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